

1/6

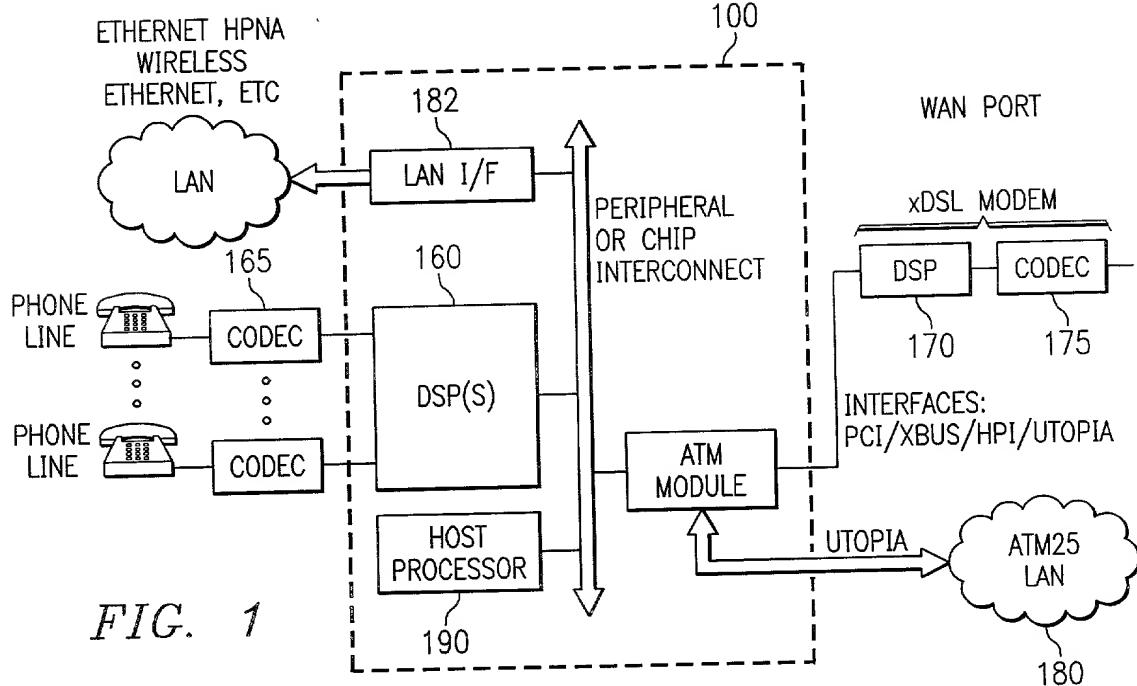


FIG. 1

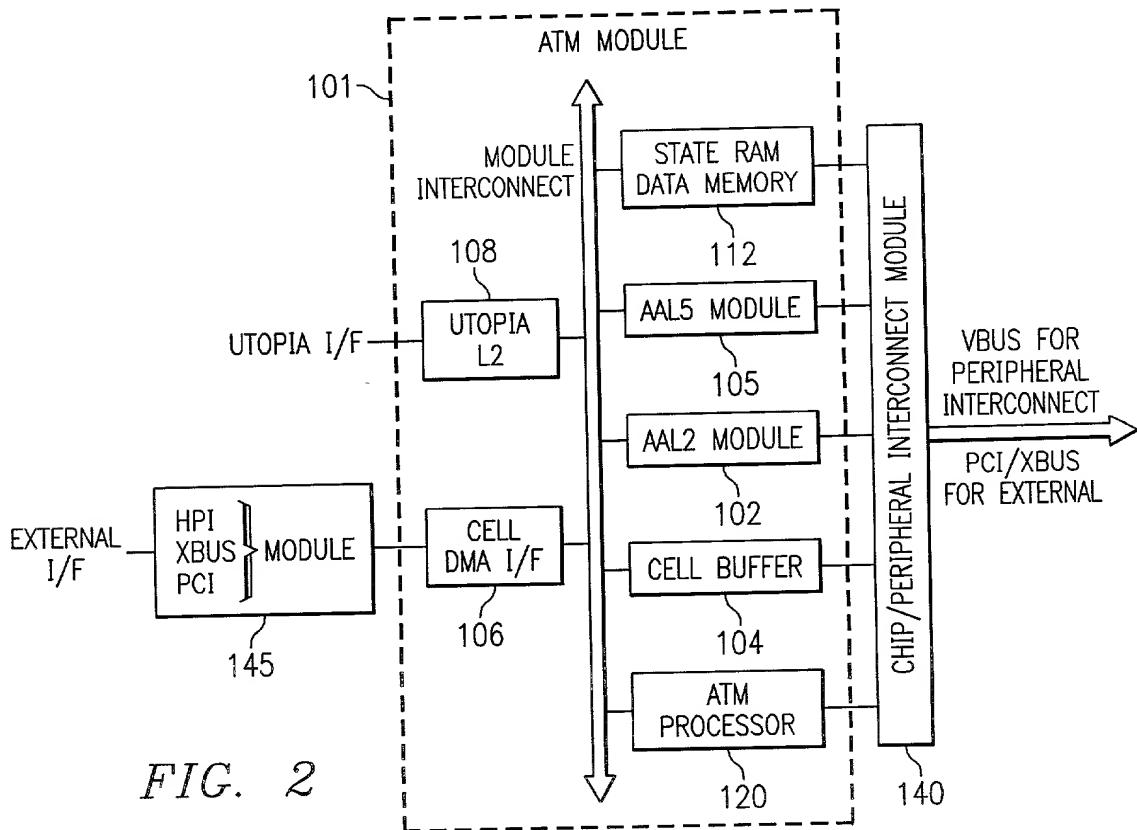


FIG. 2

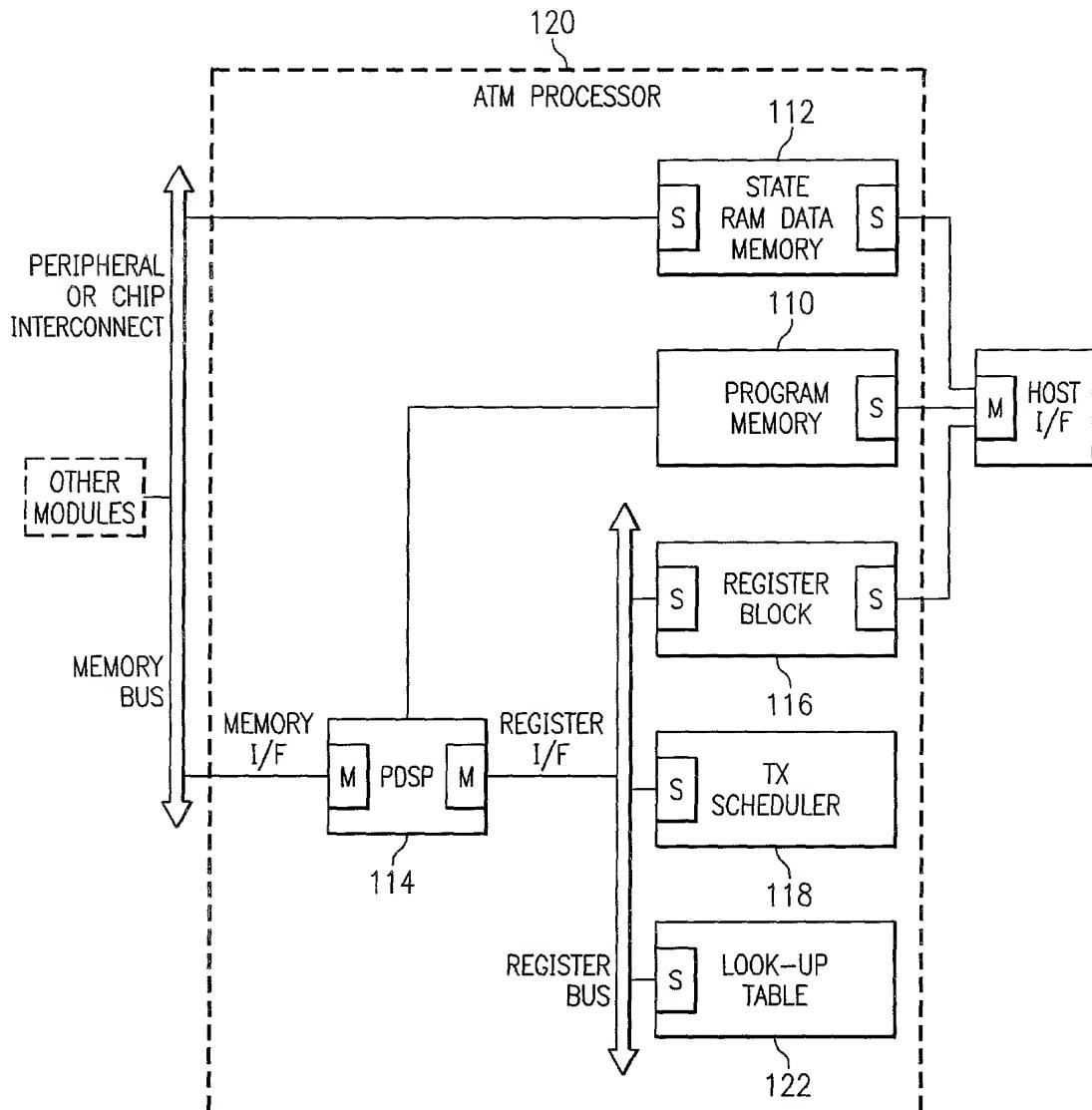


FIG. 3

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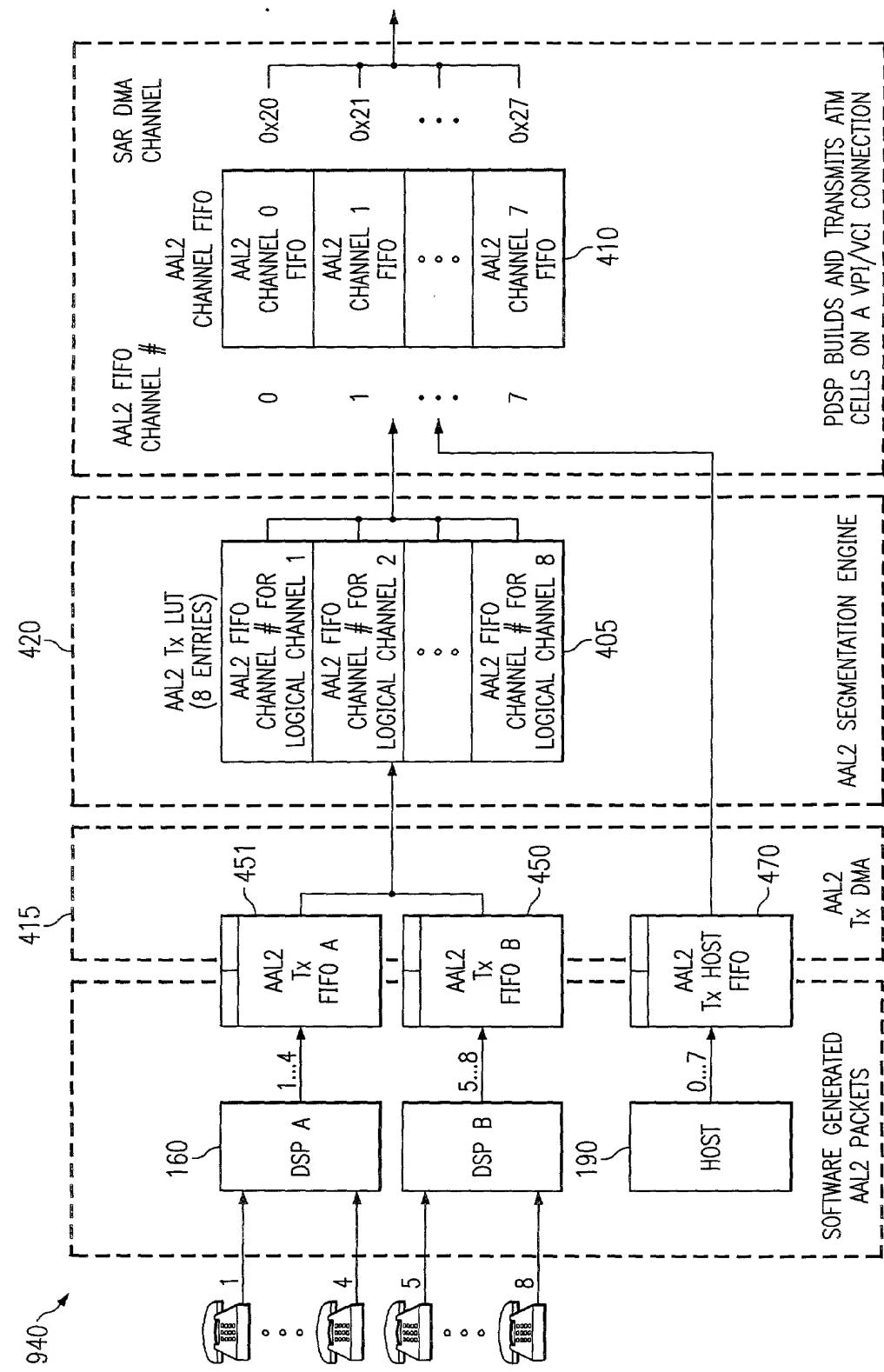
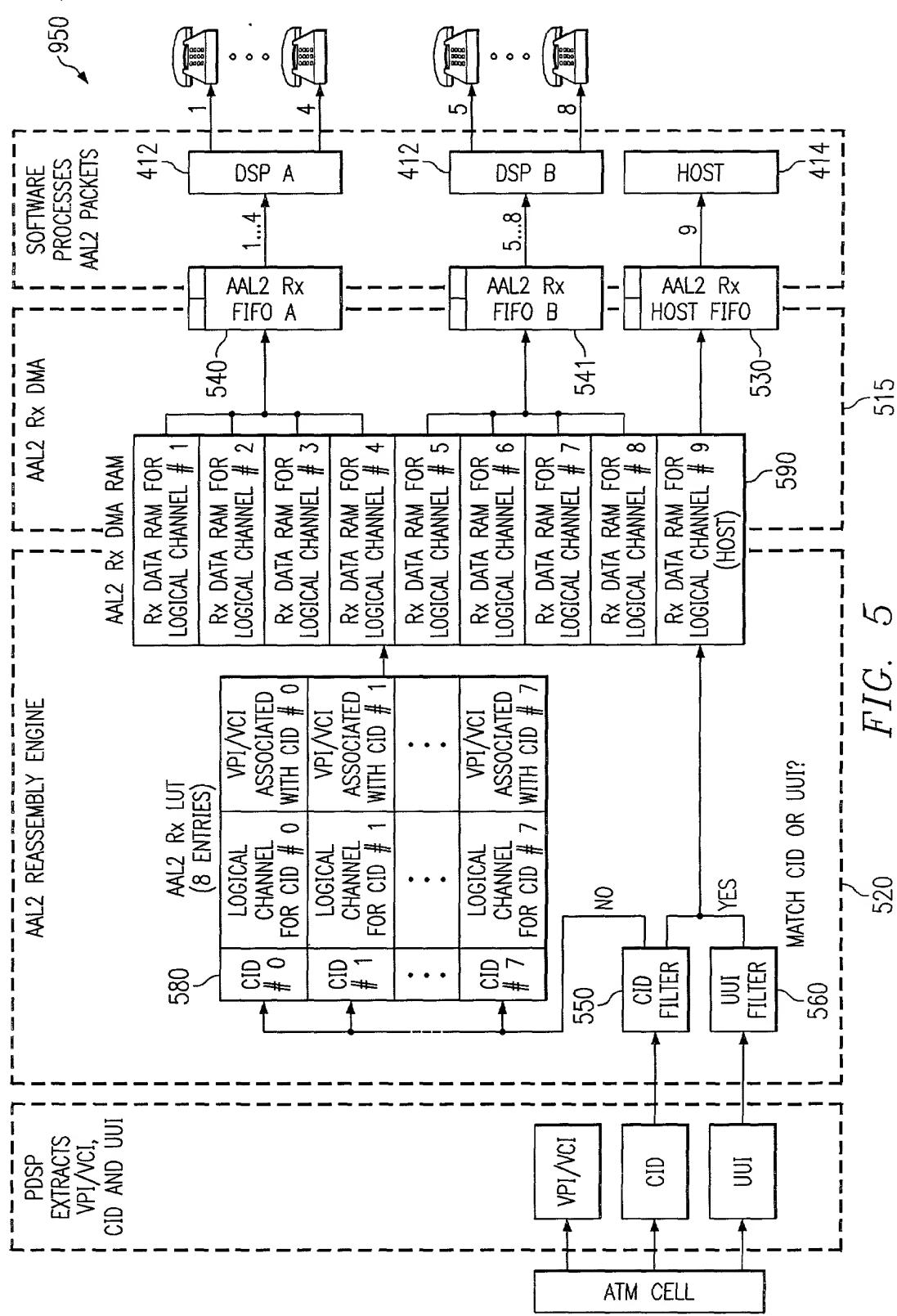


FIG. 4



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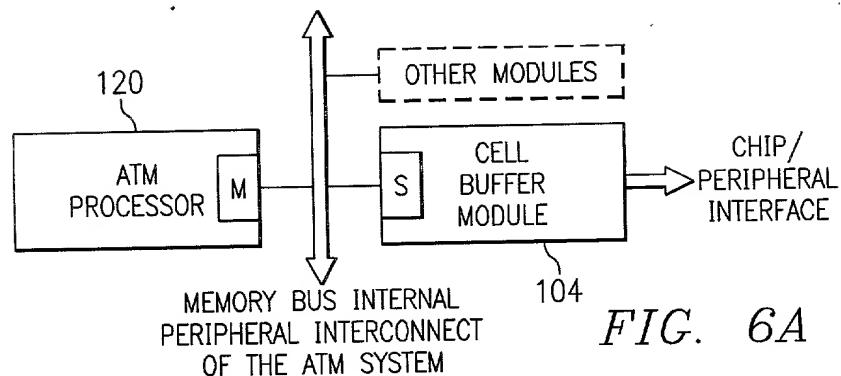


FIG. 6A

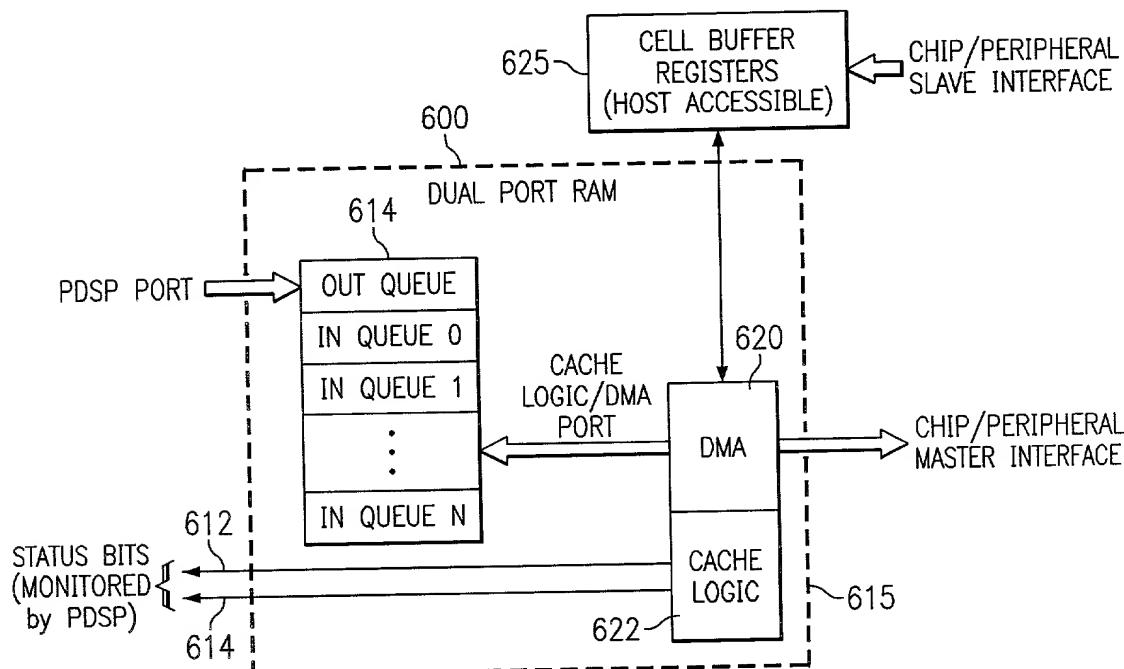


FIG. 6B

56-BYTES ALIGNED ATM CELL

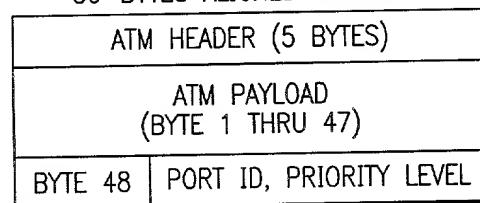


FIG. 7

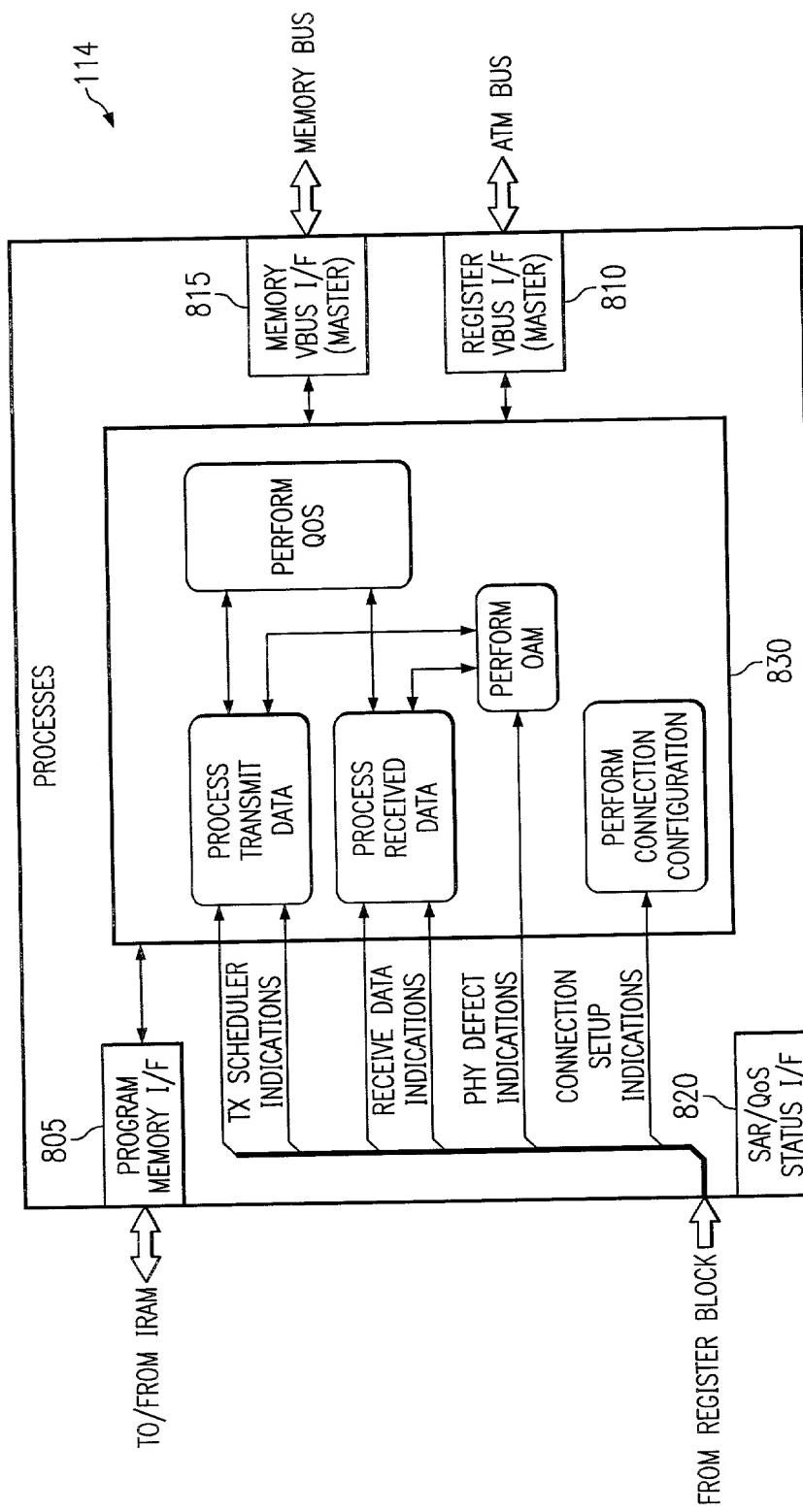


FIG. 8